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APRIL 2012 747 A Low-Power Block-Based CMOS Image Sensor With Dual VDD Qing Gao, Student Member, From Phototransduction to Imager Processing.

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Abstract A CMOS image sensor ADC using delta-sigma modulation is presented and discussed. Experimental results from a test chip are presented.

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A current mode active pixel with high sensitivity

to the traditional voltage mode APS for focal-plane image processing Eds.), CMOS Imagers: From Phototransduction to Image Processing, Kluwer

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4 backside illuminated (bsi) complementary

CMOS Imagers: From phototransduction to image processingSpringer (2004) 18. J. Shah, M.E. Jernigan, R.I. Hornsey; Characterization and deblurring of lateral crosstalk

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Analysis of noise characteristics for the active pixels in cmos image sensors for x-ray imaging. Phototransduction to Image Processing Publishers, 2004.

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Merging cmos and image processing - digital

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